

## DECLARATION FOR PATENT APPLICATION (WITH POWER OF ATTORNEY)

As an inventor named below or on any attached continuation page, I hereby declare that:

My residence, post office address and citizenship are as stated next to my name.

I believe that I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled DEVICE AND METHOD FOR ISOLATING A SHORT-CIRCUITED INTEGRATED CIRCUIT (IC) FROM OTHER IC'S ON A SEMICONDUCTOR WAFER, the specification of which (check one):

is attached hereto.

was filed on \_\_\_\_\_ as United States application serial no. \_\_\_\_\_ and was amended on \_\_\_\_\_.

was filed on \_\_\_\_\_ as PCT international application no. \_\_\_\_\_ and was amended under PCT Article 19 on \_\_\_\_\_.

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose to the U.S. Patent and Trademark Office all information known to me to be material to the patentability of the subject matter claimed in this application, as "materiality" is defined in Title 37, Code of Federal Regulations § 1.56.

I hereby claim foreign priority benefits under Title 35, United States Code, § 119 (a)-(d) or § 365(b) of any foreign application(s) for patent or inventor's certificate or § 365(a) of any PCT international application(s) designating at least one country other than the United States of America listed below and on any attached continuation page and have also identified below and on any attached continuation page any foreign application for patent or inventor's certificate or any PCT international application(s) designating at least one country other than the United States of America having a filing date before that of the application(s) on which priority is claimed.

Prior foreign/PCT application(s):

			Priority Claimed	
(number)	(country)	(day/month/year filed)	Yes	No
(number)	(country)	(day/month/year filed)	Yes	No

I hereby claim the benefit under Title 35, United States Code, § 120 of any United States application(s) or § 365(c) of PCT international application(s) designating the United States of America listed below and on any attached continuation page and, insofar as the subject matter of each of the claims of this application is not disclosed in any such prior application in the manner provided by the first paragraph of Title 35, United States Code, § 112, I acknowledge the duty to disclose to the U.S. Patent and Trademark Office all information known to me to be material to patentability as defined in Title 37, Code of Federal Regulations § 1.56 which became available between the filing date of such prior application and the national or PCT international filing date of this application:

(application serial no.)	(filing date)	(status - pending, patented or abandoned)
(application serial no.)	(filing date)	(status - pending, patented or abandoned)
(provisional application no.)	(filing date)	
(provisional application no.)	(filing date)	
(provisional application no.)	(filing date)	

I hereby claim the benefit under Title 35, United States Code, § 119(e) of any United States provisional application(s) listed below:

(provisional application no.)	(filing date)
(provisional application no.)	(filing date)
(provisional application no.)	(filing date)

I hereby appoint the following Registered Practitioners to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith:

David V. Trask, Reg. No. 22,012  
Laurence B. Bond, Reg. No. 30,549  
Allen C. Turner, Reg. No. 33,041  
Keith L. Hargrove, Reg. No. 34,836  
Edgar R. Cataxinos, Reg. No. 39,931

William S. Britt, Reg. No. 20,969  
Joseph A. Walkowski, Reg. No. 28,765  
Kent S. Birmingham, Reg. No. 30,453  
Robert G. Winkle, Reg. No. 37,474  
Michael L. Lynch, Reg. No. 30,871

Thomas J. Rossa, Reg. No. 26,799  
James R. Duzan, Reg. No. 28,393  
Julie K. Morris, Reg. No. 33,263  
Patrick McBride, Reg. No. 39,295  
Lia M. Pappas, Reg. No. 34,095

Address all correspondence to:

Jim Duzan, telephone no. (801) 532-1922.  
TRASK, BRITT & ROSSA  
P.O. BOX 2550  
Salt Lake City, Utah 84110

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Full name of first joint inventor: Warren M. Farnworth

Inventor's signature Warren M. Farnworth

Residence: Nampa, ID

Citizenship: USA

Post Office Address: 2004 S. Banner, Nampa, ID 83686

Date 4-20-92

**DECLARATION FOR PATENT APPLICATION**  
(continuation page)

Invention title: DEVICE AND METHOD FOR ISOLATING A SHORT-CIRCUITED INTEGRATED CIRCUIT (IC) FROM OTHER IC'S ON A SEMICONDUCTOR WAFER

Inventor name(s) appearing on first declaration page: Warren M. Farnworth

Additional original, first and joint inventor(s):

Full name of second joint inventor: William K. Waller

Inventor's signature

Date

Residence: Boise, ID

Citizenship: USA

Post Office Address: 3736 Mountain View Drive, Boise, ID 83704

Full name of third joint inventor: Leland R. Beffa

Inventor's signature

Date

4/20/98

Residence: Boise, ID

Citizenship: USA

Post Office Address: 6803 Diamond Street, Boise, ID 83709

Full name of fourth joint inventor: Raymond J. Beffa

Inventor's signature

Date

4/20/98

Residence: Boise, ID

Citizenship: USA

Post Office Address: 11966 Goldenrod Drive, Boise, ID 83713

Full name of fifth joint inventor: Eugene H. Cloud

Inventor's signature

Date

4-22-98

Residence: Boise, ID

Citizenship: USA

Post Office Address: 2145 Mortimer Drive, Boise, ID 83712

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I believe that I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled **DEVICE AND METHOD FOR ISOLATING A SHORT-CIRCUITED INTEGRATED CIRCUIT (IC) FROM OTHER IC'S ON A SEMICONDUCTOR WAFER**, the specification of which (check one):

is attached hereto.

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(number)	(country)	(day/month/year filed)	Yes	No
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 Michael L. Lynch, Reg. No. 30,871

Thomas J. Rossa, Reg. No. 26,799  
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Full name of first joint inventor: Warren M. Farnworth

Inventor's signature \_\_\_\_\_ Date \_\_\_\_\_

Residence: Nampa, ID

Citizenship: USA

Post Office Address: 2004 S. Banner, Nampa, ID 83686

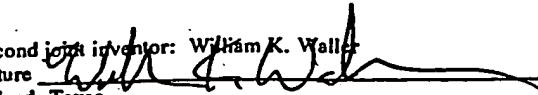
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Inventor name(s) appearing on first declaration page: Warren M. Farnworth

Additional original, first and joint inventor(s):

Full name of second joint inventor: William K. Waller

Inventor's signature 

Date

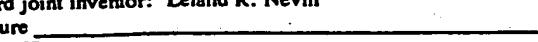
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Residence: Garland, Texas

Citizenship: USA

Post Office Address: 5626 Duck Creek Drive, Apt, D, Garland, TX 75043

Full name of third joint inventor: Leland R. Nevill

Inventor's signature 

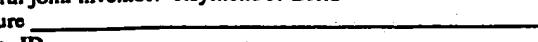
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Residence: Boise, ID

Citizenship: USA

Post Office Address: 6803 Diamond Street, Boise, ID 83709

Full name of fourth joint inventor: Raymond J. Beffa

Inventor's signature 

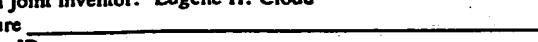
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Residence: Boise, ID

Citizenship: USA

Post Office Address: 11966 Goldenrod Drive, Boise, ID 83713

Full name of fifth joint inventor: Eugene H. Cloud

Inventor's signature 

Date

Residence: Boise, ID

Citizenship: USA

Post Office Address: 2145 Mortimer Drive, Boise, ID 83712